

FIG. 1 A

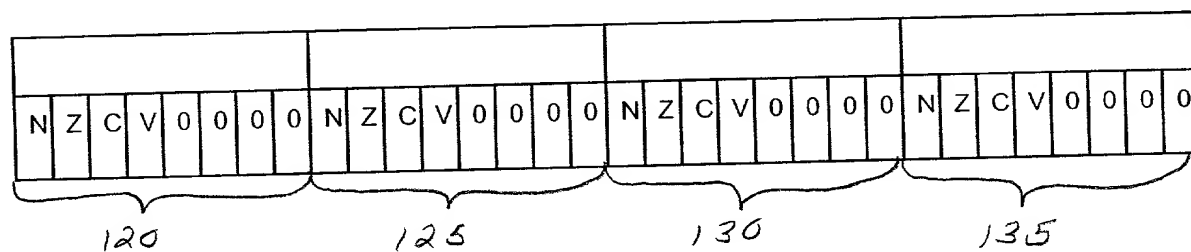


FIG. 1 B

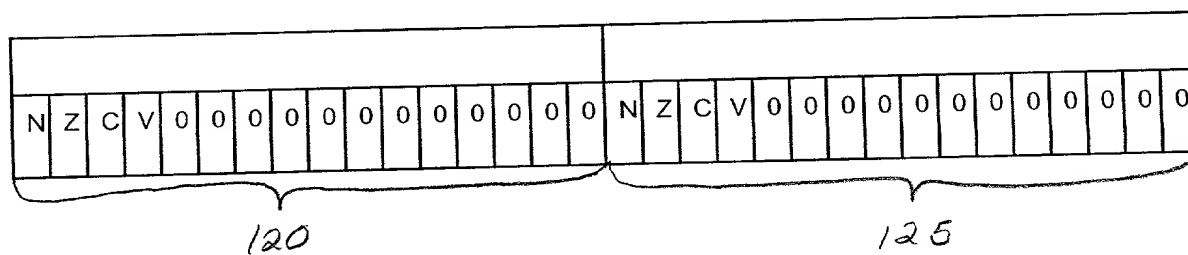


FIG. 1 C

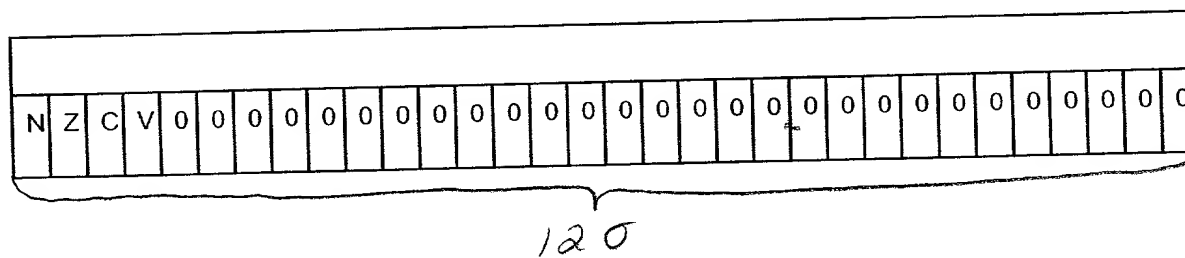
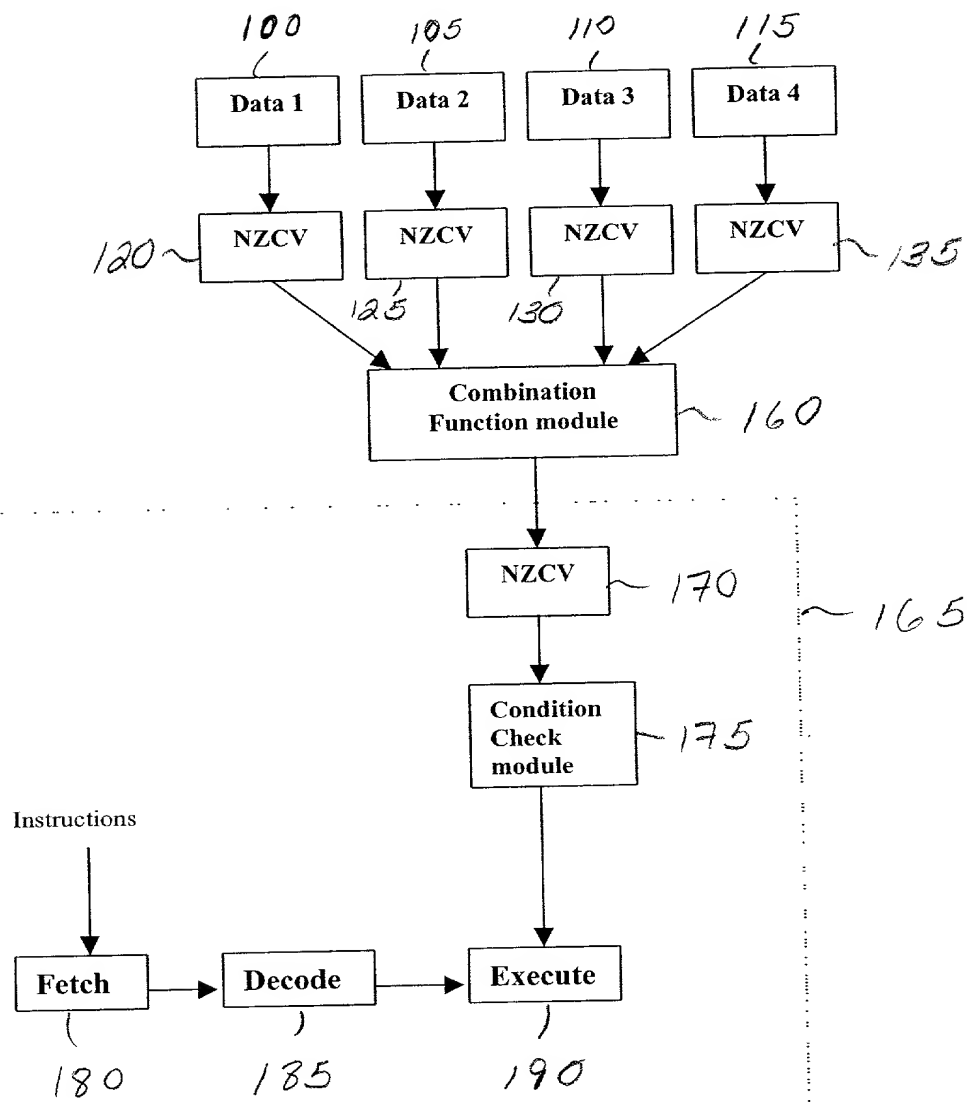


FIG. 1 D



MicroProcessor Pipeline

FIG. 2

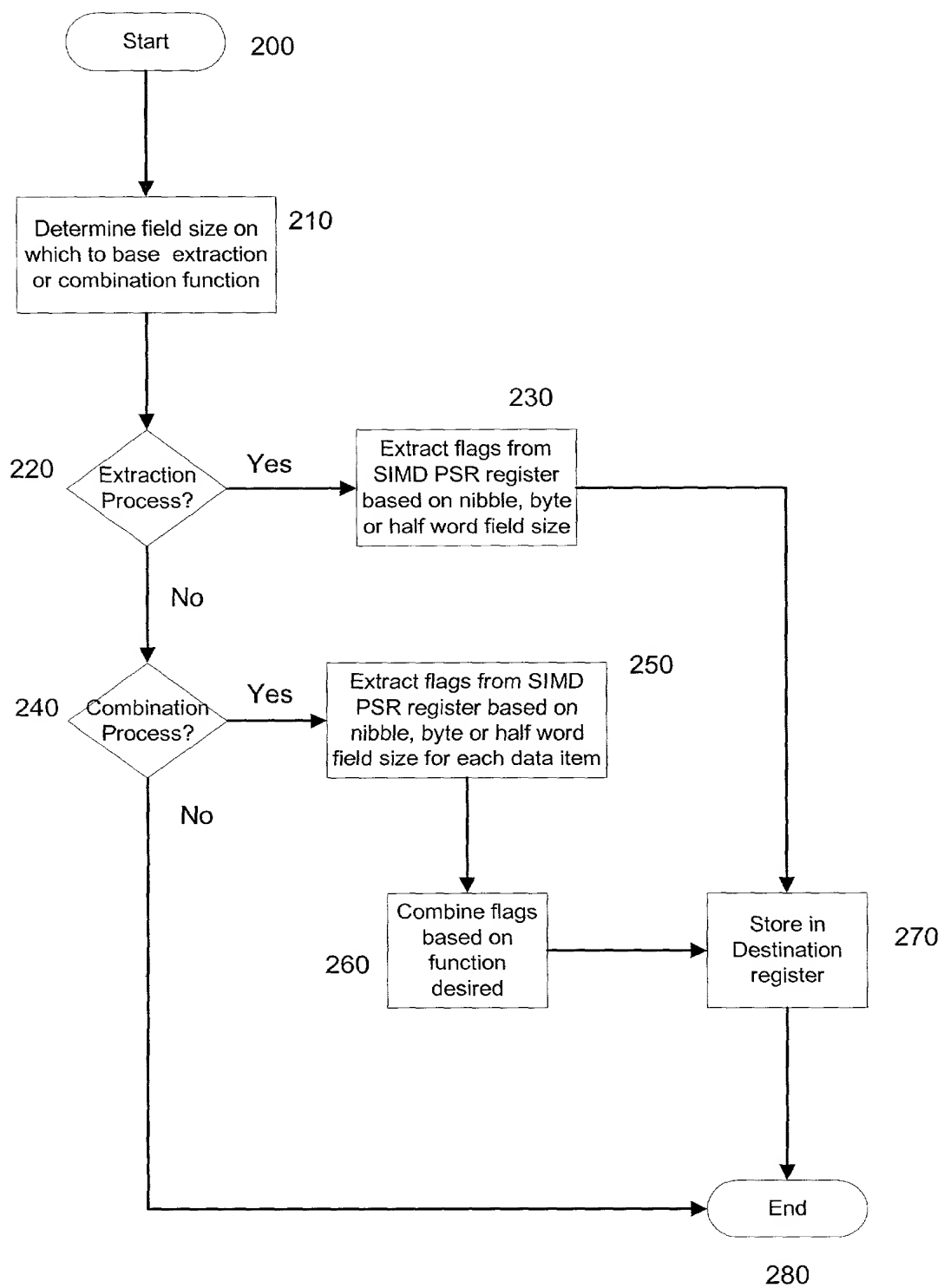


FIG. 3

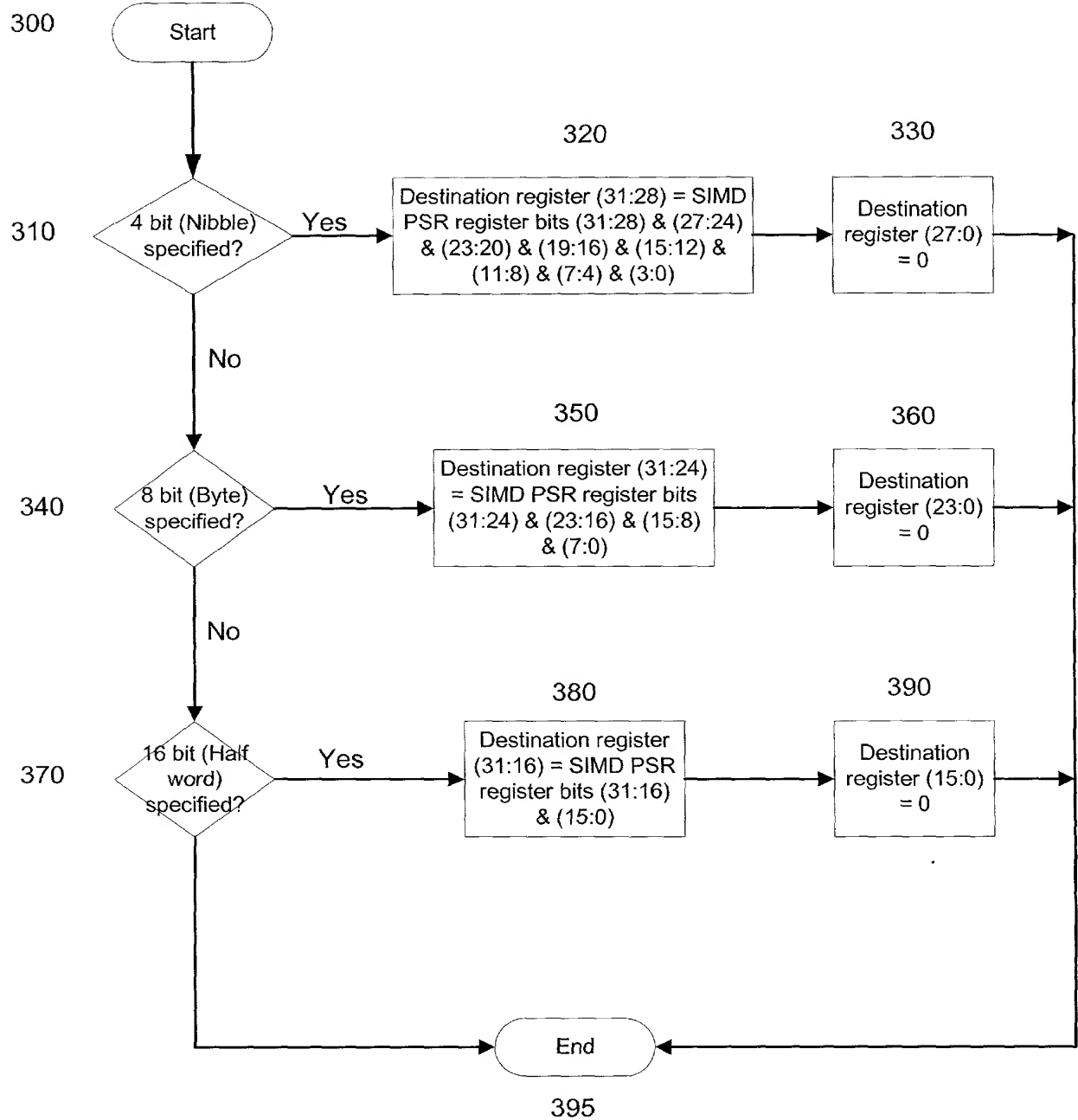


FIG. 4

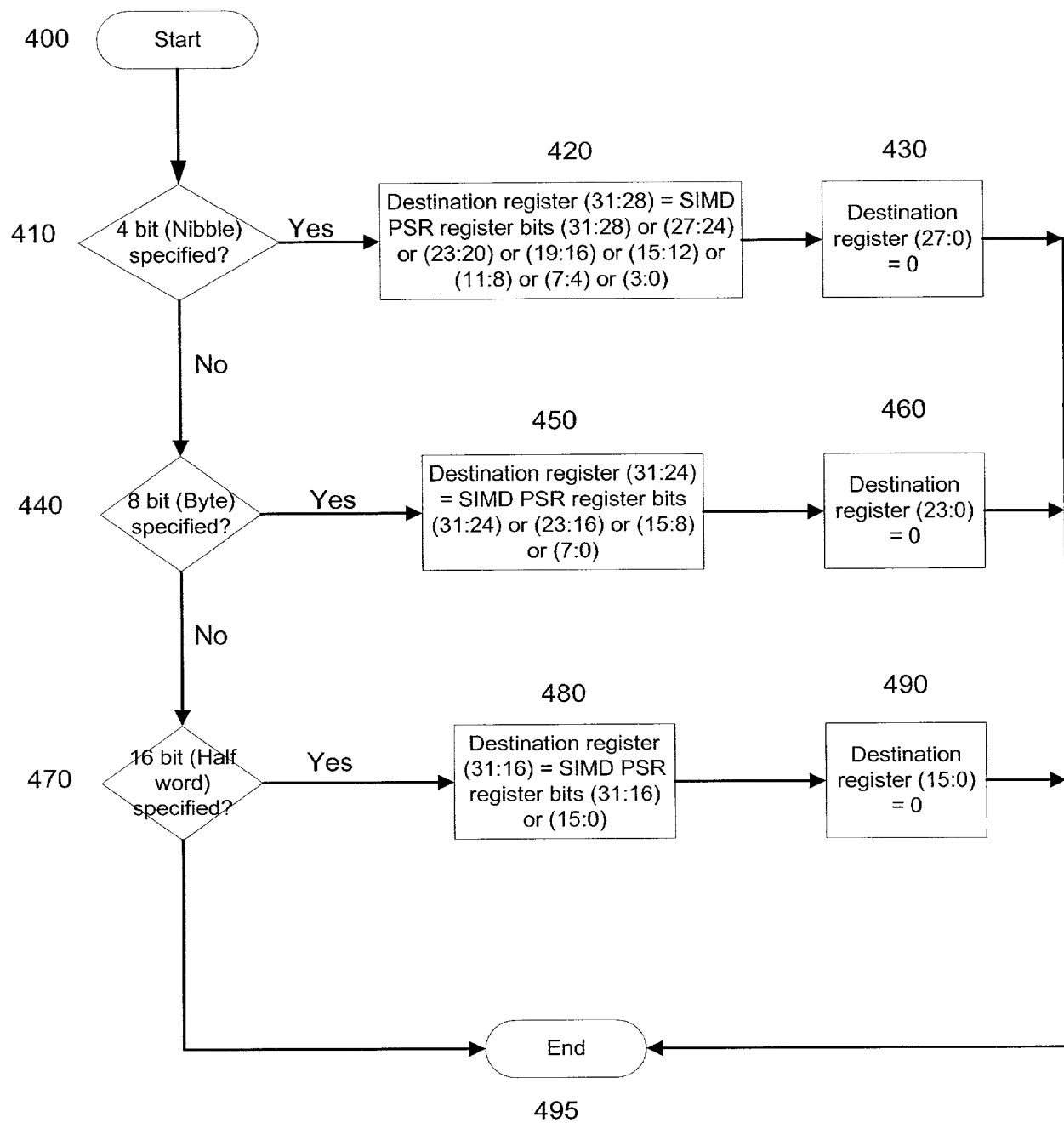


FIG. 5

